

ITW



PATENT  
Docket No. 8947-000068/US

IN THE U.S. PATENT AND TRADEMARK OFFICE

Application No.: 10/761,239

Group Art Unit: 2816

Filing Date: January 22, 2004

Examiner: Terry Lee Englund

Applicant: Jong-Hyun CHOI

Title: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE WITH  
DUAL INSULATION SYSTEM (AS AMENDED)

Attorney Docket: 8947-000068/US

**REPLY TO NOTICE OF NON-COMPLIANT AMENDMENT**

Customer Service Window  
Randolph Building  
401 Dulany Street  
Alexandria, VA 22314

July 17, 2008

Sir:

In reply to the Notice of Non-Compliant Amendment mailed July 10, 2008, Applicants attach hereto a revised version of the claims section of the Amendment filed on April 24, 2008.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 08-0750 for any additional fee required under 37 C.F.R. §§ 1.16 or 1.17; particularly, extension of time fees.

Respectfully submitted,

HARNESS, DICKEY & PIERCE, P.L.C.

By

John A. Castellano, Reg. No. 35,094

P.O. Box 8910  
Reston, VA 20195  
(703) 668-8000

JAC/pw

Attachments Revised Claims Section of April 24, 2008 Amendment

(Rev. 01/22/01)